

METHOD AND SYSTEM FOR MANAGING MEMORY
IN A MULTIPROCESSOR SYSTEM

ABSTRACT OF THE DISCLOSURE

5 A method and system for managing memory in a
multiprocessor system includes defining the plurality of
processor coherence domains within a system coherence
domain of the multiprocessor system. The processor
coherence domains each include a plurality of processors
and a processor memory. Shared access to data in the
processor memory of each processor coherence domain is
provided only to elements of the multiprocessor system
within the processor coherence domain. Non-shared access
10 to data in the processor memory of each processor
coherence domain is provided to elements of the
multiprocessor system within and outside of the processor
coherence domain.